Contents
----------

Startup
JD (JTAG Device) Index in Chain
Total (Devices in JTAG Chains)
BSDL File
Other Devices in JTAG Chain4
Prefix Instruction Length4
Post Instruction Length4
Target Class
Can Use Cable WE4
Flash Category4
Flash Name5
CS, OE, WE
Data and Address Buses
NAND Flash
Optional Settings
Data Bus Driver Dir Control
WP (Write Protect)
ALE (When Address & Data Bus are Multiplexed)13
LEDs (Lighten)
Other Controls
Save15
Verification15
Modify16

Note: There may be some changes between versions. So, your software may look a bit different from this manual.

## Startup

After software launched, select Menu Tools / Edit Configuration File... . See screenshot below:

Flash Programmer			x
File Target Device JTAG Device Options	Tools Help		
	Edit Configuration File		*
			-
4			۱.
FSL_P2020RDB_BSTJFP(NOR@CS0).ini	Idle	Success	B and

A wizard is shown as below:

Edit Wizard	×
Edit Current Cfg File	
Create a New Cfg File Edit Other Existing Cfg File	
OK Cancel	

Please choose:

- Edit Current Cfg File: The software will open currently loaded configuration file for editing. *Note: If no configuration file loaded successfully, this operation is invisible.*
- Create a New Cfg File: Create a new configuration file.
- Edit Other Existing Cfg File: Please select an existed file to edit.

UI screenshot:

Edit Configuration File	~	•		• 1,5++ • pil				
Open Configuration File					Open E	SDL File		
JD Index in Chain: 0 Total:	1 Pre	fix Instr	uction L	ength:		Po	ostfix Instruction Length:	
Target Class:	an Use Ca	ble WE#	Flas	h Category:	Flash N	lame:	▼	
Description	Dir	MC	MV	BSC(s)	Active	Inactive		<u>^</u>
WP#(Write Protect)	out	false	false		0	1		E
CS#/CE#(Chip Select/Enable	) out	false	false		0	1		
OE#/RE#(Output/Read Enab	le) out	false	false		0	1		
WE#(Write Enable)	out	false	false		0	1		
JD Data Bus Driver Dir Conti	rol out	false	false		1	0		
JD ALE(Address & Data Mux	ed) out	false	false		1	0		•
Ed	it Done	Cano	el Edit	]				
				~ ~				Up Dn
Save Exit	_	_	_					

Generally speaking, if you are creating a new configuration file, you should follow steps listed below one by one. If you are editing an existed file, you may choose items you want to edit.

### JD (JTAG Device) Index in Chain

Please input index of JTAG device that the Flash is attached. Index begins from 0. If not mentioned, JD or 'JTAG device' means the unique device in chain that is connected with Flash.

### **Total (Devices in JTAG Chains)**

Please input number of total devices in the JTAG chain. The number begins with 1.

### **BSDL File**

Click Button **Open BSDL file...** and select BSDL file for JTAG device. We'll use mcf54450.bsd as an example in this manual.

# **Other Devices in JTAG Chain**

If there's only one device in chain, or you have all BSDL files of all devices in chain, please skip this section.

### **Prefix Instruction Length**

Please input JTAG instruction length of all other devices ahead of JTAG device. If there are more than one device, please split them with '|' character. For example, '4 | 5' means there are two devices ahead of JTAG device in JTAG chain, and device at index 0 has a 4-bit JTAG instruction while device at index 1 has a 5-bit JTAG instruction. And you could find that the JTAG device is at index 2.

#### **Post Instruction Length**

Please input JTAG instruction length of all other devices behind of JTAG device. Refer to <u>Prefix</u> <u>Instruction Length</u>.

# **Target Class**

It may be Flash, SRAM or Other.



**'SRAM'** means IC who has a SRAM style CPU interface, such as a CPLD, FPGA or ASIC. **'Other'** means component that has no CPU interface.

# **Can Use Cable WE**

Reserved. Left unchecked.

# **Flash Category**

NOR or NAND.

## **Flash Name**

Click the dropdown icon to select correct Flash.

Edit Configuration File										
Open Configuration File					Оре	en BS	SDL Fi	e MCF54450.bs	d	
JD Index in Chain: 0 Total: 1	Pref	fix Instru	uction L	ength:				Postfix Instruction	Leng	gth:
Target Class: Flash 🔻 🗖 Can L	lse Cab	le WE#	Flas	h Category: NOR	Flas	h Na	ame:		-	
Description	Dir	MC	MV	BSC(s)	Acti	ve	Inac	28F320J3-8bit	Â	<u>^</u>
WP#(Write Protect)	out	false	false		0	•	1	28F640J3-8bit		E
CS#/CE#(Chip Select/Enable)	out	false	false		0	•	1	28F128J3-8bit 28F128J3-16bit		
OE#/RE#(Output/Read Enable)	out	false	false		0	-	1	28F256J3-8bit 28F256J3-16bit		
WE#(Write Enable)	out	false	false		0	•	1	28F256P30B-16bit 28F256P30T-16bit	Ξ	
JD Data Bus Driver Dir Control	out	false	false		1	-	0	28F256P33B-16bit 28F256P33T-16bit		
JD ALE(Address & Data Muxed)	out	false	false		1	-	0	MX29LV640*B-16bit MX29LV640*T-16bit		-
Edit Do	ne	Cance	el Edit	]				MX29LV320*B-16bit MX29LV320*T-16bit		
0 fb_ta_bB1 2 fb_rwbB2 4 fb_cs_b(0) C4 6 fb_cs_b(1) C3 8 fb_cs_b(2) D4 10 fb_cs_b(2) C2				▲ ▼ -> ▼ <-				EN29LV640-16bit W28J320B-16bit W28J320T-16bit S29GL032N*01-16bit S29GL032N*04-16bit S29GL032N*03-16bit S29GL064N*01-16bit		Up Dn
Exit								S29GL064N*06-16bit S29GL064N*04-16bit S29GL064N*03-16bit		

If you could not find Flash you want in the list, please select '**Customization**' and then select a Flash parameter file. See screenshot below:

Flas	h N	lame:		-
			MX29LV320*8-16bit	
A atting Taxad		Inact	MX29LV320*T-16bit	
нсш	ve	maci	EN291 V640-16bit	
<b>_</b>	-	4	W281320B-16bit	
· ·	_	T	W281320T-16bit	
	Ψ	4	S29GL032N*01-16bit	
J .		T	S29GL032N*04-16bit	
	-	4	S29GL032N*03-16bit	
<b>.</b> .	_	T	S29GL064N*01-16bit	
_	ΨÌ		S29GL064N*06-16bit	
υ.	_	1	S29GL064N*04-16bit	_
	ΨÌ	•	S29GL064N*03-16bit	
L.	_	0	S29GL032M*R0-16bit	
	$\mathbf{v}$	•	S29GL032M*R4-16bit	
1.		0	S29GL032M*R3-16bit	
			S29GL128N/P-16bit	
			S29GL256N/P-16bit	
			S29GL512N/P-16bit	
			S29GL01GP-16bit	
			M29W128GL-16bit	Ξ
			M29W128GH-16bit	
			M29W640GH-16bit	
			M29W640GL-16bit	
	_		M29W640GT-16bit	
			M29W640GB-16bit	
			M29EWF256-16bit	
	_		M29EWF512-16bit	
			SST39VF040-8bit	
			SST39VF160-16bit	
			Customization	4

Edit Configurat	tion File			23
Open Configuration	File	Open BSDL File MCF544	50.bsd	
JD Index in Chain: (	Total: 1 Prefix Instruction Length	h: Postfix Instru	ction Length:	
Target Class: Flash	Can Use Cable WE# Flash Cat	tegory: NOR - Flash Name: Customization		
Description	😓 Open	the laste		₽₽
WP#(Write Prote	🚱 🗢 📕 « Software 🕨 Self	→ flash_para → 🗸 😽	搜索 flash_para 🛛 👂	
CS#/CE#(Chip S	组织 ▼ 新建文件夹		≣ ▼ 🔟 🔞	7
WE#(Write Enab	ibis	<b>^</b> 名称 <sup>^</sup>	修改日期 类	
JD Data Bus Driv	iss ib	Dosolete	2012/6/4 19:28 文	
JD ALE(Address	📕 pdml	28F128J3-8bit.ini	2012/5/25 21:16 IN 2012/5/25 21:14 IN	-
	January Self	28F128J3-16bit_x2_as_32bit.ini	2013/2/21 22:42 IN	
0 fb_ta_b B1 2 fb_rwb B2	📗 bin	28F256J3-8bit.ini	2012/5/25 21:19 IN 2012/5/25 21:16 IN	
4 fb_cs_b(0) C4 6 fb_cs_b(1) C3	📕 doc	28F256P30B-16bit.ini	2012/5/25 21:20 IN	
10 fb cc b(3) C2	📗 flash_para		2012/5/25 21:22 IN 2012/5/25 21:20 IN	H
Save	i media	28F256P33T-16bit.ini	2012/5/25 21:22 IN	
	🍌 Setup	28F320J3-8bit.ini	2012/5/25 21:21 IN 2012/5/25 21:20 IN	
	🔒 SWIFT	▼	2012/3/23 21:20 IN. •	
	File name:	•	Flash Parameters Files (*.ini) 🔹	
			Open Cancel	

If you could not find Flash parameter file, please contact us.

# CS, OE, WE

Set chip select pin for Flash.

Click **CS#/CE#** row, you will see all available pins for CS. See screenshot below:

Edit Configuration File	-			_ <b>0 X</b>
Open Configuration File			Open BSDL File MCF 54450.bsd	
JD Index in Chain: 0 Total: 1	Prefix Ins	truction Length:	Postfix Instruction Length:	
Target Class: Flash 🔻 🗌 Can U	se Cable WE	# Flash Category: NC	R    Hash Name: 28F320J3-8bit	
Description	Dir MC	MV BSC(s)	Active Inactive	<u>^</u>
WP#(Write Protect)	out false	e false		E
CS#/CE#(Chip Select/Enable)	out false	e false Click	0 1 1	
OE#/RE#(Output/Read Enable)	out false	e false	0 • 1 •	
WE#(Write Enable)	out false	e false	0 • 1 •	
JD Data Bus Driver Dir Control	out false	e false	1 • 0 •	
JD ALE(Address & Data Muxed)	out false	e false	1 • 0 •	-
Edit Do	ne Can	ncel Edit		
0         fb_ta_bB1         All           2         fb_rwb B2         Available           4         fb_cs_b(0) C4         Available           6         fb_cs_b(1) C3         Pins           8         fb_cs_b(2) D4         List           10         fb_cs_b(2) E2         List           Save         Exit         Exit			Selected Pin List	

©Hangzhou Zhefar Technologies Co., Ltd. For more info please visit <u>http://www.zhefar.com</u> Each line of pin in list contains three items:



For example: '0 fb\_ta\_b B1' means cell number 0, port name (pin name in most cases) is fb\_ta\_b, and pin location is B1.

Text in difference color corresponds with difference item in screenshot above.

Supposed Flash CS pin is connected to CPU pin fb\_cs\_b(0) (pin location C2). You can use a filter to find target pin very quickly. The keyword could be cell number, port name or pin location.



You could input 'C4', you will see:

C4	Edit Done Cancel Edit
4 fb_cs_b(0) C4	

You could also input '4', and then you will see:



Of course, you may input pin name 'cs'. See screenshot below:

cs	Edit Done Cancel Edit	
4 fb_cs_b(0) C4 6 fb_cs_b(1) C3 8 fb_cs_b(2) D4 10 fb_cs_b(3) C2 182 sd_cs_b(0) R6 184 sd_cs_b(1) P6		4 III >

After you have found '4 fb\_cs\_b(0) C2', select it and click '->' button to add it to selected pin list.

cs	Edit Done Cancel Edit
4         fb_cs_b(0) C4           6         fb_cs_b(1) C3           8         fb_cs_b(2) D4           10         fb_cs_b(3) C2           182         sd_cs_b(0) R6           184         sd_cs_b(1) D6	Selected pin list
Save Exit	

#### Tips: Double click also works.

*Tips: If there is only one line in all pin lists, you could press 'Enter' key to add it to selected pin list.* 

You will see that '4 fb\_cs\_b(0) C2' now added. Then click '**Edit Done**' button. See screenshot below:

Edit Configuration File					
Open Configuration File					Open BSDL File MCF54450.bsd
JD Index in Chain: 0 Total: 1	Pre	fix Instri	uction L	ength:	Postfix Instruction Length:
Target Class: Flash 💌 🗆 Can U	se Cab	le WE#	Flas	h Category: NOR	▼ Flash Name: 28F320J3-8bit ▼
Description	Dir	MC	MV	BSC(s)	Active Inactive
WP#(Write Protect)	out	false	false		
CS#/CE#(Chip Select/Enable)	out	false	false		
OE#/RE#(Output/Read Enable)	out	false	false		0 • 1 •
WE#(Write Enable)	out	false	false		
JD Data Bus Driver Dir Control	out	false	false		
JD ALE(Address & Data Muxed)	out	false	false		1 • 0 •
Edit Do	ne	Cance	el Edit	]	
0 fb_ta_bB1 2 fb_rwbB2 6 fb_cs_b(1)C3 8 fb_cs_b(2)D4 10 fb_cs_b(3)C2 12 ostddata(0)D3				▲ ▼ 4 ft -> ▼ <-	b. <u>cs. b(0) C4</u> Up Dn
SaveExit					

In the table, CS is assigned a pin whose BSC cell number is 4. See screenshot below:

#### JFP Edit Cfg File User Manual

Edit Configuration File									
Open Configuration File					Ope	n BS	DL File	MCF54450.bsd	]
JD Index in Chain: 0 Total: 1	Pre	fix Instr	uction L	ength:				Postfix Instruction Length:	
Target Class: Flash 🔻 🗌 Can U	se Cab	ole WE#	Flas	h Category: NOR	Flas	n Na	me: 28F	F320J3-8bit 🔻	
Description	Dir	MC	MV	BSC(s)	Activ	e	Inactive	•	<u>^</u>
WP#(Write Protect)	out	false	false		0 -	•	1 💆		Ξ
CS#/CE#(Chip Select/Enable)	out	false	false	$\bigcirc$	0	•	1 -		
OE#/RE#(Output/Read Enable)	out	false	false		0	•	1 -		
WE#(Write Enable)	out	false	false		0	•	1 -		
JD Data Bus Driver Dir Control	out	false	false		1 .	-	D _		
JD ALE(Address & Data Muxed)	out	false	false		1 .	•	o _		-
Edit Do	ne	Cance	el Edit	]					
0 fb_ta_bB1 2 fb_rwbB2 6 fb_cs_b(1)C3 8 fb_cs_b(2)D4 10 fb_cs_b(3)C2 12 ortidata(0)D3				▲ ▼ 4 fb_c	<u>s_b(0)</u>	C4			Up Dn
Exit									

Please check whether Active value for CS is right or not. See screenshot below:

Edit Configuration File			
Open Configuration File			Open BSDL File MCF54450.bsd
JD Index in Chain: 0 Total: 1	Prefix Ir	nstruction L	n Length: Postfix Instruction Length:
Target Class: Flash 🗸 Can U	se Cable W	E# Flas	lash Category: NOR ▼ Flash Name: 28F320J3-8bit ▼
Description	Dir M	C MV	BSC(s) Active Inactive
WP#(Write Protect)	out fal	se false	e 0 • 1 •
CS#/CE#(Chip Select/Enable)	out fal	se false	e 4 0 1 1
OE#/RE#(Output/Read Enable)	out fal	se false	e 0 • 1 •
WE#(Write Enable)	out fal	se false	e 0 • 1 •
JD Data Bus Driver Dir Control	out fal	se false	
JD ALE(Address & Data Muxed)	out fal	se false	ie 1 • 0 •
Edit Do	ne Ca	ancel Edit	t
0 fb_ta_b B1 2 fb_rwb B2 6 fb_cs_b(1) C3 8 fb_cs_b(2) D4 10 fb_cs_b(3) C2 12 ortHdstaf(0) D3 Save Exit			▲ ▼ 4 ⓑ cs b(0) C4 Up >> ▼ <- Dn

Please set **OE#** and **WE#** in similar steps as **CS#/CE#**. Screenshot below is a reference that both **OE#** and **WE#** are set.

Edit Configuration File								
Open Configuration File					Open E	SDL File.	MCF54450.bsd	
JD Index in Chain: 0 Total: 1	Pre	fix Instr	uction L	ength:			Postfix Instruction Length:	
Target Class: Flash 🔻 🗌 Can U	se Cab	le WE#	Flas	h Category: NOR	Flash N	lame: 2	8F320J3-8bit 🔻 🛄	
Description	Dir	MC	MV	BSC(s)	Active	Inactiv	e	
WP#(Write Protect)	out	false	false		0 -	1 -	<b>-</b>	E
CS#/CE#(Chip Select/Enable)	out	false	false	4	0 💌	1 -	<u>-</u>	
OE#/RE#(Output/Read Enable)	out	false	false	438	0 💌	1 -	<u>-</u>	
WE#(Write Enable)	out	false	false	2	0 💌	1 _	<u>-</u>	
JD Data Bus Driver Dir Control	out	false	false		1 -	0	<u>-</u>	
JD ALE(Address & Data Muxed)	out	false	false		1 -	0	<u>-</u>	-
Edit Do	ne	Cance	el Edit					
0 fb_ta_bB1 4 fb_cs_b(0) C4 6 fb_cs_b(1) C3 8 fb_cs_b(2) D4 10 fb_cs_b(3) C2 12 ostddata/0 D3				▲ ▼ 2 fb_r	wb B2			Up Dn
SaveExit								

### **Data and Address Buses**

Click **Data Bus** row, and now we'll add data bus from MSB to LSB (as far as Flash is concerned). In example of this manual, Flash pin D7 is connected to CPU pin fb\_ad(31). So we must find fb\_ad(31) and add it first. See screenshot below:

Open Configuration File       Open BSDL File       MCF54450.bsd         JD Index in Chain:       0       Total:       1       Prefix Instruction Length:       Postfix Instruction Length:         Target Class:       Flash	x
JD Index in Chain:       0       Total:       1       Prefix Instruction Length:       Postfix Instruction Length:         Target Class:       Flash <ul> <li>Can Use Cable WE#</li> <li>Flash Category:</li> <li>NOR</li> <li>Flash Name:</li> <li>28F32033-8bit</li> <li></li> <li>Description</li> <li>Dir</li> <li>MC</li> <li>MV</li> <li>BSC(s)</li> <li>Active</li> <li>Inactive</li> <li>WE#(Write Enable)</li> <li>out</li> <li>false</li> <li>false</li> <li>1</li> <li>v</li> <liv< li=""> <li>v</li> <li>v</li> <li< td=""><td></td></li<></liv<></ul>	
Target Class:       Flash       Can Use Cable WE#       Flash Category:       NOR       Flash Name:       28F320J3-8bit          Description       Dir       MC       MV       BSC(s)       Active       Inactive         WE#(Write Enable)       out       false       false       1          JD Data Bus Driver Dir Control       out       false       false       1	
Description     Dir     MC     MV     BSC(s)     Active     Inactive       WE#(Write Enable)     out     false     false     2     0     1     -       JD Data Bus Driver Dir Control     out     false     false     1     -     0     -	
WE#(Write Enable)       out       false       false       2       0       1       I         JD Data Bus Driver Dir Control       out       false       false       1       I       0       I	*
JD Data Bus Driver Dir Control out false false 1 • 0 •	
	Ξ
JD ALE(Address & Data Muxed) out false false 1 - 0	
Data Bus 1, Click true false N/A N/A	
in true false N/A N/A	
out true false N/A N/A	-
fb_ad 2, Use filter Edit Done Cancel Edit	
364 fb_ad(31) A14     3. Select     Up       366 fb_ad(30) A13         368 fb_ad(29) D12         370 fb_ad(28) C12         372 fb_ad(27) B12         Save     Exit	

Edit Configuration File									
Open Configuration File					Open B	SDL File		MCF54450.bsd	
JD Index in Chain: 0 Total: 1	Pre	fix Instr	uction L	ength:			Post	fix Instruction Length:	
Target Class: Flash 🔹 🗖 Can U	se Cal	ole WE#	Flas	h Category: NOR	Flash N	lame:	28F320	0J3-8bit 🔻	
Description	Dir	MC	MV	BSC(s)	Active	Inacti	ve		*
WE#(Write Enable)	out	false	false	2	0 💌	1 .	-		
JD Data Bus Driver Dir Control	out	false	false		1 -	0	-		E
JD ALE(Address & Data Muxed)	out	false	false		1 -	0	•		
Data Bus		true	false		N/A	N/A			
	in	true	false		N/A	N/A			
	out	true	false		N/A	N/A			-
fb_ad Edit Do	ne	Cance	el Edit	]					
366 fb_ad(30) A13 368 fb_ad(29) D12 370 fb_ad(28) C12 372 fb_ad(27) B12 374 fb_ad(25) A12 376 fb_ad(25) A12 376 fb_ad(25) D11 Save				▲ ● 364 fb.	ad(31) A	14			Up Dn

Then  $fb_ad(30)$ . See screenshot below.

Edit Configuration File								
Open Configuration File					Open E	SDL File	MCF54450.bsd	
JD Index in Chain: 0 Total: 1	Pref	fix Instru	uction L	ength:		P	ostfix Instruction Length:	
Target Class: Flash  Can Us	se Cab	le WE#	Flas	h Category: NOR	Flash N	lame: 28F	32033-8bit 🔻	
Description	Dir	MC	MV	BSC(s)	Active	Inactive		<b>^</b>
WE#(Write Enable)	out	false	false	2	0 –	1 -		
JD Data Bus Driver Dir Control	out	false	false		1 -	0 💌		E
JD ALE(Address & Data Muxed)	out	false	false		1 -	0 💌		
Data Bus		true	false		N/A	N/A		
	in	true	false		N/A	N/A		
	out	true	false		N/A	N/A		-
fb_ad Edit Dor	ne	Cance	el Edit	]				
368 fb_ad(29) D12 370 fb_ad(28) C12 372 fb_ad(27) B12 374 fb_ad(26) A12 376 fb_ad(25) D11 378 fb_ad(25) D11				▲ ▼ 364 fb_ 366 fb_ > <	ad(31) A ad(30) A	14 13		Up Dn
SaveExit								đ

After all eight data buses are added, click 'Edit Done' button.

Edit Configuration File				
Open Configuration File			Open BSDL File MCF54450.bsd	
JD Index in Chain: 0 Total: 1	Prefix Instr	uction Length:	Postfix Instruction Length:	
Target Class: Flash  Can Use	e Cable WE#	Flash Category: NOR 🔻	Flash Name: 28F320J3-8bit •	
Description	Dir MC	MV BSC(s)	Active Inactive	<b>^</b>
WE#(Write Enable)	out false	false 2	0 • 1 •	
JD Data Bus Driver Dir Control	out false	false	1 0 -	E
JD ALE(Address & Data Muxed)	out false	false	1 • 0 •	
Data Bus	true	false	N/A N/A	
	in true	false	N/A N/A	
	out true	false	N/A N/A	-
fb_ad Edit Don	e Canc	el Edit		
380       fb_ad(23)       B11         382       fb_ad(22)       A11         384       fb_ad(21)       D10         386       fb_ad(20)       C10         388       fb_ad(20)       C10         388       fb_ad(20)       C10         390       fb_ad(19)       B10         390       fb_ad(19)       B10         394       fb_ad(16)       C9         395       fb_ad(16)       C9         396       fb_ad(15)       B9         398       fb_ad(13)       D8		▲ ● 364 fb_ 366 fb_ 366 fb_ 370 fb_ 370 fb_ 372 fb_ 374 fb_ 378 fb_	ad(31) A14 ad(30) A13 ad(29) D12 ad(28) C12 ad(27) B12 ad(25) A12 ad(25) D11 ad(24) C11	

Address Bus row is edited similarly as Data Bus.

Attention: If Flash data bus width is not 8-bit, please add the LSB address bus of CPU in 'Address Bus' row. If 16-bit mode, Flash address bus pin A0(maybe A-1) is not connected with LSB address bus pin of CPU, but you should add the LSB in the list. To 32-bit mode, last tow LSB pins should be added. In fact, you could specify any output pin as the last pin(s), because it's not really used.

### **NAND** Flash

**ALE, CLE** and **I/O Bus** are for NAND Flash. Please left blank if you are using NOR Flash. Otherwise, please set them.

## **Optional Settings**

#### **Data Bus Driver Dir Control**

If any data bus driver exists between CPU and Flash, please let the software know which pin is controlling the direction of the buffer driver.

Active means from CPU to peripheral bus ('Write'), Inactive means peripheral bus to CPU ('Read').

Left blank if you are not using any bus driver.

For example: The 245 driver's DIR pin is controlled by CPU's R/W pin. We know R/W pin will output 'High' when reading and 'Low' when writing. On the other hand, 'Active' value is for 'Write' operation, so the 'Active' value should be 0, and 'Inactive' value should be '1'.

#### WP (Write Protect)

Set which pin of JTAG device is controlling WP pin of Flash. Active means Flash is protected from writing, **Inactive** means no write protection. If WP is not controlled or Flash has no WP pin, left this row blank.

#### ALE (When Address & Data Bus are Multiplexed)

To some CPU (e.g. MPC8548, MPC8313), their address bus and data bus are multiplexed. You should select which pin is used to the address bus latch. Active means latch IC is able to latch address.

### LEDs (Lighten)

If there are LEDs connected to JTAG device directly, please add them to the list. Active means lighten, Inactive means darken. You may add multi LEDs.

Note: If LEDs are controlled by some data bus driver or flip-flops, do not set them.

#### **Other Controls**

In example in this manual, we want to control other chip select pins to high.

There are five pins:  $fb_cs_b(1)$ ,  $fb_cs_b(2)$ ,  $fb_cs_b(3)$ ,  $sd_cs_b(0)$ ,  $sd_cs_b(1)$ .

You must set the value at the same time.

Click '**Other Controls**' row, and you will see a drop list is ready for your choice. See screenshot below:

JFP	Edit	Cfg	File	User	Manual
	Lun	CIS	1 IIC	0.501	manual

Edit Configuration File				
Open Configuration File			Open BSDL File MCF54450.bsd	
JD Index in Chain: 0 Total: 1	Prefix Instr	ruction Length:	Postfix Instruction Length:	
Target Class: Flash  Can Us	e Cable WE#	Flash Category: NOR	▼ Flash Name: 28F320J3-8bit ▼	
Description	Dir MC	MV BSC(s)	Active Inactive	<b>^</b>
IO Bus	true	false	N/A N/A	
	in true	false	N/A N/A	
	out true	false	N/A N/A	
LEDs(Lighten)	out true	false	0 - 1 -	
Other Controls <sup>1, Click</sup>	out true	true	N/A	E
				-
Edit Dor	ne Cano	el Edit		
0 fb_ta_bB1 2 fb_rwb B2 4 fb_cs_b(0) C4 6 fb_cs_b(1) C3 8 fb_cs_b(2) D4 10 fb_cs_b(3) C2 12 pstddata(0) D3 14 pstddata(1) E4 16 pstddata(2) C1 18 pstddata(3) D2 20 pstddata(4) F3 Save Exit		* •		Up Dn

Select pin fb\_cs\_b(1) and set the value to '1' to disable the chip select signal connected to other component. Then click '->' button. See screenshot below:

Open Configuration File       Open BSDL File       MCF54450.bsd         JD Index in Chain:       0       Total:       1       Prefix Instruction Length:         Target Class:       Flash	Edit Configuration File				
DD Index in Chain:       0       Total:       1       Prefix Instruction Length:       Postfix Instruction Length:         Target Class:       Flash	Open Configuration File		Open BSD	DL File MCF54450.bsd	
Target Class:       Flash           Can Use Cable WE # Flash Category: NOR ▼ Flash Name: 28F320J3-8bit ▼         Description       Dir       MC       MV       BSC(s)       Active       Inactive         IO Bus       true       false       N/A       N/A       Inactive         IO Bus       true       false       N/A       N/A         out       true       false       N/A       N/A         Outer Controls       out       true       false       N/A         Other Controls       out       true       N/A       N/A         G       Edit Done       Cancel Edit       Image: Display State       Image: Display State       Image: Display State       Image: Display State         4       fb_cs_b(0) C4       Image: Display State       I	JD Index in Chain: 0 Total: 1	Prefix Instruction Leng	gth:	Postfix Instruction Length:	
Description       Dir       MC       MV       BSC(s)       Active       Inactive         IO Bus       true       false       N/A       N/A       N/A         in       true       false       N/A       N/A         out       true       false       N/A       N/A         LEDs(Lighten)       out       true       false       0       1         Other Controls       out       true       n/A       N/A         Gs       Edit Done       Cancel Edit       Up       Dn         10       fb.cs.b(0) C4       0       1       0       Dn         6       fb.cs.b(2) D4       0       0       0       Dn         10       fb.cs.b(2) D4       0       0       0       0       Dn         182 sd.cs.b(0) R6	Target Class: Flash 🔻 🗖 Can Us	n Use Cable WE# Flash Ca	Category: NOR   Flash Nam	me: 28F320J3-8bit 🔹	
IO Bus     true     false     N/A     N/A       in     true     false     N/A     N/A       out     true     false     N/A     N/A       LEDs(Lighten)     out     true     false     0     1       Other Controls     out     true     true     N/A	Description	Dir MC MV BS	SC(s) Active In	inactive	*
in     true     false     N/A     N/A       out     true     false     N/A     N/A       LEDs(Lighten)     out     true     false     0     1       Other Controls     out     true     true     N/A       Cs     Edit Done     Cancel Edit       4     fb_ccs b(1) C3     1       8     fb_ccs b(2) D4     0       10     fb_ccs b(3) C2     1       184 edit cs_b (b) R6	IO Bus	true false	N/A N	N/A	
out     true     false     N/A     N/A       LEDs(Lighten)     out     true     false     0 ▼ 1     ▼       Other Controls     out     true     true     N/A		in true false	N/A N	N/A	
LEDs(Lighten) out true false 0   1 Other Controls out true true N/A CS Edit Done Cancel Edit 4 fb_cs_b(0) C4 6 fb_cs_b(2) D4 1 0 fb_cs_b(3) C2 182 sd_cs_b(0) R6 C-		out true false	N/A N	V/A	
Other Controls         out         true         N/A           cs         Edit Done         Cancel Edit           4         fb_cs_b(0) C4         Image: Cancel Edit           5         fb_cs_b(2) D4         Image: Cancel Edit           10         fb_cs_b(3) C2         Image: Cancel Edit           182 sd_cs_b(0) R6         Image: Cancel Edit         Image: Cancel Edit	LEDs(Lighten)	out true false	0 📕 1	<b>_</b>	
cs         Edit Done         Cancel Edit           4         fb_cs_b(0) C4         Image: Cancel Edit         Up           6         fb_cs_b(2) C4         Image: Cancel Edit         Up           6         fb_cs_b(2) C4         Image: Cancel Edit         Up           10         fb_cs_b(2) C4         Image: Cancel Edit         Up           10         fb_cs_b(3) C2         Image: Cancel Edit         Up           184 edit cs_b(0) R6         Image: Cancel Edit         Image: Cancel Edit         Image: Cancel Edit	Other Controls	out true true	N	N/A	E
CS         Edit Done         Cancel Edit           4         fb_cs_b(0) C4         Image: black of the state of the					*
4 fb_cs_b(0) C4 6 fb_cs_b(1) C3 8 fb_cs_b(2) D4 10 fb_cs_b(3) C2 182 sd_cs_b(0) R6 C-	CS Edit Dor	Done Cancel Edit			
238     dspi pcs_1P14       242     dspi pcs_5N14       246     dspi pcs_0R16	4 fb_cs_b(1) C4 6 fb_cs_b(1) C3 8 fb_cs_b(2) D4 10 fb_cs_b(3) C2 182 sd_cs_b(0) R6 184 sd_cs_b(1) P6 238 dspi_pcs_1P14 242 dspi_pcs_5 N14 246 dspi_pcs_0 R16 Save Exit				Up Dn

You know, to a given pin, you should know which state ('0' or '1') is that you want. We want to disable chip select  $fb_cs_b(1)$ , so we set it to '1'.

Add left pins. Screenshot shows all pins are set.

JFP Edit	Cfg File	User	Manual
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🖪 Edit Configuration File							
Open Configuration File Open BSDL File MCF54450.bsd							
JD Index in Chain: 0 Total: 1	JD Index in Chain: 0 Total: 1 Prefix Instruction Length: Postfix Instruction Length:						
Target Class: Flash 🔻 🗌 Can Us	e Cable WE#	Flas	h Category: NOR	Flash Name: 28F320	J3-8bit 🔻		
Description	Dir MC	MV	BSC(s)	Active	Inactive	^	
IO Bus	true	false		N/A	N/A		
	in true	false		N/A	N/A		
	out true	false		N/A	N/A		
LEDs(Lighten)	out true	false		0 _	1 -		
Other Controls	out true	true	6 8 10 182 184	11111	N/A	E	
Edit Done Cancel Edit							
0       fb_ta b B1       ▲         2       fb_rwb B2       ■         4       fb_cs_b(0) C4       B8         12       pstddata(0) D3       B8         14       pstddata(1) E4       Image: Display in the system       Image: Display in the system         16       pstddata(3) D2       Image: Display in the system       Image: Display in the system       Image: Display in the system         20       pstddata(3) D2       Image: Display in the system       Image: Disp							

### Save

Click Save... button to save your editing.

## Verification

Close **Edit Configuration File** dialog and return to main UI. Select menu **File / Open Configuration File...** . See screenshot below:

😵 Flash Programmer					x
File Target Device JTAG Device	Options	Tools	Help		
Open Configuration File					~
Exit					
					~
•					Þ
FSL_P2020RDB_BSTJFP(NOR@CS0).ii	ni		Idle	Success	B

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🕹 Open		×
Goovernment → 计算机 → Data (E	) ▶	٩
组织 ▼ 新建文件夹	8==	• 🔳 🔞
	▲ 名称 ▲	修改日期
▲ 🧊 库	\$RECYCLE.BIN	2012/10/23 11:5
	🐌 lic	2013/7/2 14:13
	My Documents	2012/6/4 19:27
▲ 🖳 计算机	System Volume Information	2012/10/11 4:48
🛛 🕹 Windows (C:)	= 🔑 Work	2012/12/16 22:4
⊳ 🧰 Software (D:)	temp_BSTJFP.ini	2013/7/27 12:56
4 💼 Data (E:)		
B SRECYCLE.BIN		
🐌 lic		
🛛 🐌 My Documents		÷.
File <u>n</u> ame: tem	D_BSTJFP.ini	on Files (*.in 🔻
	<u>O</u> pen	Cancel

Select the file saved just now. It's e:\temp\_BSTJFP.ini in this example.

If this operation finished without any error, you will see configuration filename is showed in status bar. It's temp\_BSTJFP.ini in this example.

😵 Flash Programmer						x
<u>File Target Device J</u>	TAG Device 🤇	Options	<u>T</u> ools	<u>H</u> elp		
I						*
•						P P
temp_BSTJFP.ini				Idle	Success	B

Please continue 'Modify' in next section if there is any error.

# Modify

#### Select menu Tools / Edit Configuration File... .

Then check row by row.

Note: If your BSDL file in configuration file is parsed successfully, you should not to select BSDL file again.

Save after modification is done.

Date	Version	Author	Changes
2020/1/5			Remove file format section when saving file;
2019/6/7			Add 'Maybe A-1' to A0;
2016/5/12			Add an example for DIR control of data buffer;
2014/6/9			Add line above footer;
2013/10/14			Optimization for easier reading;
2013/7/30			Mistyping correction;
2013/7/27			First Release

#### **Revision History**